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AVR Instruction Set Manual Microchip Technology

December 4th, 2018 - AVR Microcontrollers AVR Instruction Set Manual OTHER Instruction Set Nomenclature Status Register SREG SREG Status Register C Carry Flag Z Zero Flag

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avr libc lt avr sfr defs h gt Special function registers

November 23rd, 2018 - The compiler will choose the correct instruction sequence to generate based on the address of the register being accessed The advantage of using the memory mapped

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Instruction set architecture Wikipedia

December 6th, 2018 - Overview An instruction set architecture is distinguished from a microarchitecture which is the set of processor design techniques used in a particular processor

AVR microcontrollers Wikipedia

December 6th, 2018 - The AVR instruction set is more orthogonal than those of most eight bit microcontrollers in particular the 8051 clones and PIC microcontrollers with which AVR

avr libc lt avr sleep h gt Power Management and Sleep Modes

December 7th, 2018 - Use of the SLEEP instruction can allow an application to reduce its power consumption considerably AVR devices can be put into

different sleep modes

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